

PATENT



DOCKET NO.: Intel 2207/11839

ASSIGNEE: Intel Corporation

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANTS : Michael D. RUEHLE
SERIAL NO. : 09/888,105
FILED : June 22, 2001
FOR : METHOD AND APPARATUS FOR ACTIVE MEMORY
BUS PERIPHERAL CONTROL UTILIZING ADDRESS
CALL SEQUENCING
GROUP ART UNIT : 2181
EXAMINER : TBA

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Barbara Vance

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UNDER 37 C.F.R. 1.97(d)(1)**

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Submitted herewith is an Information Disclosure Statement under 37 C.F.R. §1.97(d)(1), Form PTO-1449 (modified), and four (4) reference copies.

This Information Disclosure Statement is provided due to the receipt of a Search Report for the corresponding PCT application, which PCT search Report references documents which were not submitted in the Information Disclosure Statement provided by applicants in the present patent application.

Applicants do not believe there is any fee due in connection with the filing of this Information Disclosure Statement under 37 C.F.R. §1.97(d)(1).

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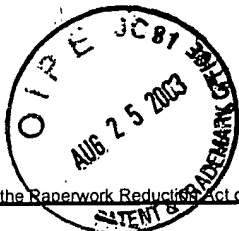
KENYON & KENYON

Dated: August 20, 2003

By: Stephen T. Neal
Stephen T. Neal
(Reg. No. 47,815)
Attorneys for Intel Corporation

KENYON & KENYON
333 West San Carlos St., Suite 600
San Jose, CA 95110

Telephone: (408) 975-7500
Facsimile: (408) 975-7501



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TRANSMITTAL FORM (to be used for all correspondence after initial filing)	Application Number	09/888,105	
	Filing Date	June 22, 2001	
	First Named Inventor	Michael D. RUEHLE	
	Art Unit	2181	
	Examiner Name	TBA	
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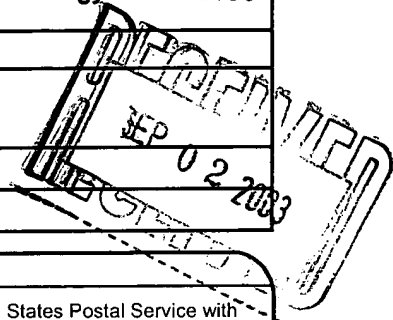
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Firm or Individual name	Stephen T. Neal (Reg. No. 47,815) Attorneys for Intel Corporation
Signature	<i>Stephen T. Neal</i>
Date	August 20, 2003



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